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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/601,147	06/19/2003	Lawrence C. Gunn III	LUX-P004	7226

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Fernandez & Associates, LLP
PO Box D
Menlo Park, CA 94026-6402

EXAMINER

CHIEM, DINH D

ART UNIT PAPER NUMBER

2883

DATE MAILED: 02/10/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/601,147

Applicant(s)

GUNN ET AL.

Examiner

Erin D Chiem

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-39 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-39 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 6/19/03 is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. ____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date ____.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: ____.

DETAILED ACTION

Drawings

1. The drawings are objected to as failing to comply with 37 CFR 1.84(p)(5) because they include the following reference character(s) not mentioned in the description: Fig. 2 (203).

Corrected drawing sheets in compliance with 37 CFR 1.121(d), or amendment to the specification to add the reference character(s) in the description in compliance with 37 CFR 1.121(b) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

2. In addition to Replacement Sheets containing the corrected drawing figure(s), applicant is required to submit a marked-up copy of each Replacement Sheet including annotations indicating the changes made to the previous version. The marked-up copy must be clearly labeled as "Annotated Marked-up Drawings" and must be presented in the amendment or remarks section that explains the change(s) to the drawings. See 37 CFR 1.121(d). Failure to timely submit the proposed drawing and marked-up copy will result in the abandonment of the application.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

4. Claims 1 –11, 14-25, and 28-30, 31, 37 – 39 are rejected under 35 U.S.C. 102(a) as being anticipated by Bosco et al. (US 2003/0015707 A1). Figure 58, Bosco et al. disclose an array of optical grating coupler 5106 fabricated on a compound monocrystalline semiconductor material 5118. The monocrystalline semiconductor material have been previously disclosed has being selectable from the group of, for example, gallium arsenide, gallium indium arsenide, gallium aluminum arsenide, indium phosphide, cadmium sulfide, cadmium mercury telluride, zinc selenide, zinc sulfur selenide, and the like in any of the Group IIA and VA elements, mixed II-V compounds, Group II and VIA elements, and mixed III-VI compounds [0042]. Upon the substrate 5118, and array of waveguides 5126 are optically aligned to the array of optical grating couplers 5106. The devices used in Bosco's demultiplexer include photodiodes 5110, waveguides 5126, transimpedance amplifier 5112 etc. In operation, light in the optical waveguides 5126 impinges on the photodiodes 5110, producing electrical signals related to the split optical signal in the waveguide 5126. The transimpedance amplifier 5112 amplifies the signal produced by the photodiode 5110 and produces an output signal at the signal output 5114. See [0209]. Furthermore, in Fig. 59, the transistors that are on the substrate in the optical grating couplers 5106 are visible, even though Bosco et al. do not explicitly disclose the transistors in the disclosure of this specific embodiment. And it is well known in the art that the most common

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method of forming transistors is with the CMOS process [0114]. Regarding claims 6 – 7 and 21 – 22, respectfully, through the Applicant's own admittance that c4 solder bonds employing gold bumps are "commonly involved in commercial bump bonding... [and] flip-chip operations. See claim 7, 21, and 22. Regarding claims 14, 15, 29 and 30, Bosco et al. disclose the method of fabricating semiconductor structure utilizing the formation of a compliant substrate from monocrystalline materials. Bosco et al. teach employing bonding the template material to the surface of the buffer layer at selective sites to provide sites for the nucleation of the epitaxial growth of the monocrystalline material layer [0043]. And upon this grown layer, the unwanted insulating layers are etched away to form the electro-optic devices upon the substrate [0121].

5. Regarding claim 31, in one embodiment of the structure, in which one skilled in the art can fabricate optical devices such as array of optical grating couplers, waveguides, photodiodes, and similar optical components, Bosco et al. disclose a monocrystalline substrate 22 is a silicon substrate and further having a buffer zone, a second substrate, made of indium-phosphide; the substrates are chemically bonded together [0055 – 0056], [0043].

6. Regarding claims 37 – 39, Figure 31 and 32 demonstrate the well-known assembly of a device use to convert optical signal to electricity and vice versa with a combination of devices comprising a photo emitter or optical laser, array optical grating, photodetector, a bipolar junction, and a transistor, etc. [0140]. Furthermore, Bosco et al. disclose how light distribution is performed through the AWGM 5006, 5010.

"This device splits the incoming light signals into an integer number m , ... identical signals. Each signal contains all of the wavelengths of the incoming signal. Each of the m signals is then fed into its own optical waveguide. The path length of each optical waveguide is designed so that there is a calculated length difference between the adjacent waveguides. Through constructive and

destructive interference, the composite waveguides and output starcoupler function as a diffraction grating separating the signal into n separate signals. The number of waveguides m determines the spacing between wavelengths... Each of these signals is then fed into its own optical waveguide. Other methods could be substituted to achieve this function, such as using a Bragg grating, and so on.” [0203].

Claim Rejections - 35 USC § 103

7. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

8. Claims 12 and 27 are rejected under 35 U.S.C. 103(a) as being unpatentable over Bosco et al. (US 2003/0015707) in view of Magne et al. (US 6,226,426 B1).

9. In Figure 58, Bosco et al. disclose an array of optical grating coupler 5106 fabricated on a compound monocrystalline semiconductor material 5118 and the array of optical grating couplers are aligned with the waveguides formed on the substrate, which are etched on, but Bosco et al. do not disclose the array of optical grating couplers is designed to match the mode field of the array of optical devices.

10. Magne et al. disclose mode matching to increase the coupling efficiency between the grating coupling to the etched zone (col. 8, line 29-33).

11. Since Bosco et al. and Magne et al. are both from the same field of endeavor, the purpose disclosed by Magne et al. would have been recognized in the pertinent art of Bosco et al.

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12. It would have been obvious at the time the invention was made to a person having ordinary skill in the art to match the fundamental mode field, the most important mode in optical transmission via waveguides. If the mode[s] is/are not matched, dispersion of the transmitting signal occurs and as equally importantly dramatic power loss occur and the transmitted signal is more prone to err when exiting from the demultiplexer.

13. Claims 32 and 34 are rejected under under 35 U.S.C. 103(a) as being unpatentable over Kamon (US 5,285,258) in view of Ford (US 6,272,272 B1) and Anderson et al. (US-2003/0057363 A1).

14. Kamon discloses having diffractive grating array formed on a semiconductor wafer acting as a alignment mark and further measures the intensity of light, the diffractive grating array is detected, which leads to accurately detecting the alignment mark (Abstract). But Kamon do not disclose aligning the first of the array of optical devices to the first of the array of optical grating couplers, then aligning the last of the array of optical devices to the last of the array of optical grating couplers, and attaching the array of optical devices to the array of optical grating couplers.

15. Ford discloses actively aligning a linear array of devices and then the aligned devices are then permanently joined by laser welding (col. 2, line 19 – 20). Furthermore, the array is aligned in the manner that the first and last elements in the array (col. 2, line 39 – 40). The motivation of employing such alignment method and attaching the devices together after alignment is intuitive. Alignment process is time consuming; therefore, one well-known in the art would find it reasonable to attach the aligned devices to prevent future realignment. It is also intuitive to align a linear array by exactly aligning the first device in the array and the last device in the array for

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the entire array to be aligned. For example, in wrapping a gift, one would place a piece of tape to hold together the seam of a present, one would accurately aim the first end of the tape and the last end of the tape at the desired position and the pieces of tape in between would follow the aligned ends.

16. Since Kamon and Ford are both from the same field of endeavor, the purpose disclosed by Ford would have been recognized in the pertinent art of Kamon.

17. It would have been obvious at the time the invention was made to a person having ordinary skill in the art to align a linear array of devices such as an array of grating couplers by aligning the first and last device for the alignment of the devices in the middle to follow.

Furthermore, active alignment is time consuming; it is intuitive for one well known in the art to attach the array of optical devices to the array of optical grating couplers to prevent alignment per use.

18. Claim 32, 35, and 36 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kamon in view of Ford as applied to claim 32 and 34 above, and further in view of Giboney et al. (US 6,318,909 B1).

19. Kamon and Ford disclose the method of attaching an array of optical devices to an array of optical grating couplers formed on a substrate by forming a plurality of alignment marks on the substrate, silicon wafer, and aligning the array of grating coupler by aligning the first and the last grating, and finally attach the grating couplers to the devices, which are now aligned. But Kamon and Ford do not disclose a method of using a vision system with pattern recognition for automated alignment.

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20. Giboney et al. disclose pattern recognition as a method employed an automatic aligning process for optical devices and the aligning members to position (col. 13, line 67 – col. 14, line 1 – 7). Alternatively, Giboney et al disclose 2 other aligning methods for alignment by (1) sending optical signals to the end of the fiber optic ribbon from the alignment connector, and determine the best signal-to-noise ratio generated by the optic device while the position of the optical device is optimized (col. 14, line 23 – 30). (2) Another method is send electrical signals to the transmitting elements via the electrical connector to cause the transmitting elements to generate optical signals. The optical signals at the end of the fiber optic ribbon remote from the alignment connector are monitored, and the position of the assembly relative to the device package is manipulated until the optical signals have a maximum signal-to-noise ratio, or some other indication of an optimal alignment of the assembly is obtained col. 14, line 11 – 23).

21. Since Kamon, Ford, and Giboney et al. are all from the same field of endeavor, the purpose disclosed by Giboney et al. would have been recognized in the pertinent art of Kamon and Ford.

22. It would have been obvious at the time the invention was made to a person having ordinary skill in the art to employ the automatic aligning process to assemble the casing which house the optical apparatus. Connecting members and the predrilled holes on the casing is typically used to hold the casing together. The examiner, respectfully, interpret the broad claim 33 to be utilize in holding the casing which house the apparatus together since the Applicant do not further indicate in the Specification as to the uniqueness or in descriptive explanation of when the automated alignment process is used in the method for attaching an array of the optical devices to an array of optical grating couplers. Furthermore, claim 35 and 36 are inventive

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definitions of active alignment method, and this is well known in the art. Any alignment linear arrays of optical devices employ the method of active alignment. Although, Giboney et al. disclose using an optic fiber ribbon, but a waveguide or optical fibers may easily replace the fiber ribbon and the same method may be used.

Conclusion

23. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Fukuda et al. (US 2004/0057653 A1) disclose the same teaching as the Applicant. Respectfully, the Examiner would like to point out that Fukuda et al. filed one month prior to the Applicant's application.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Erin D Chiem whose telephone number is (571) 272-3102. The examiner can normally be reached on Monday - Thursday 9AM - 5PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Frank G Font can be reached on (571) 272-2415. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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Erin D Chiem
Examiner
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edc

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